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Bib Data Sheet

CONFIRMATION NO. 7512

SERIAL NUMBER 10/763,255	FILING DATE 01/26/2004  RULE	CLASS 714	GROUP ART UNIT 2138	ATTORNEY DOCKET NO. R2180.0188/P188
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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\* *none pme*

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *yes pme*  
 JAPAN 2003-018920 01/28/2003

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
 \*\* 04/28/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>pme</i> Examiner's Signature Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 13	TOTAL CLAIMS 24	INDEPENDENT CLAIMS 6
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 24998  
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TITLE  
 Semiconductor integrated circuit and scan test method therefor

FILING FEE  RECEIVED 1230	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue )
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